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|---|---|---------------------------|---|---|---|---|---|---|---|----|
| <b>Dept Number</b>  | <b>CS 503</b>   | <b>Course Title</b>       | <b>Fault-Tolerant Computing Systems</b> |   |   |   |   |   |   |    |
| <b>Semester Hours</b>   | <b>3</b>  | <b>Course Coordinator</b> | <b>Bidyut Gupta</b>                     |   |   |   |   |   |   |    |
| <b>Catalog Description</b>  | An introduction to different aspects of fault-tolerance in computing systems. Redundancy techniques with an emphasis on information redundancy, software fault-tolerance, coding techniques, algorithm-based fault-tolerance, fault-tolerant interconnection network architecture, DFT techniques, and quantitative evaluation methods. |                           |   |   |   |   |   |   |   |    |
| <b>Textbooks</b>  |   |                           |   |   |   |   |   |   |   |    |
| <b>References</b>   |   |                           |   |   |   |   |   |   |   |    |
| <b>Course Learning Outcomes</b>   |   |                           |   |   |   |   |   |   |   |    |
| <ul style="list-style-type: none"> <li>• To give the students an introduction to the different aspects of fault detection, diagnosis and tolerance in computer systems in general.</li> <li>• To prepare the background such that students will be able to carry out further work in a more specialized fashion in any of these areas.</li> </ul> |   |                           |   |   |   |   |   |   |   |    |
| <b>Assessment of the Contribution to Program Outcomes</b>   |   |                           |   |   |   |   |   |   |   |    |
| <b>Outcome →</b>  | 1   | 2                         | 3                                       | 4 | 5 | 6 | 7 | 8 | 9 | 10 |
| <b>Assessed →</b>   | X   | X                         | X                                       | X | X |   | X |   |   |    |
| <b>Prerequisites by Topic</b>   |   |                           |   |   |   |   |   |   |   |    |
| CS 401.   |   |                           |   |   |   |   |   |   |   |    |

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| <b>Major Topics Covered in the Course</b> |   |               |

1. Introduction : Fault Characterization, reliability modeling, physical faults and fault models. {4 classes}
2. Test generation in digital systems : concepts, structural level and functional level test generation, random testing. {6 classes}
3. Design for testability : testability measures, scan techniques, testable networks, syndrome testability. {6 classes}
4. Fault Simulation : simulation models, algorithms for simulation and evaluation, parallel and deductive fault simulation. {6 classes}
5. Coding Techniques: parity check, unidirectional, arithmetic and communication codes and properties, self-checking circuits, fault-tolerant combinational and sequential machines. {6 classes}
6. System Diagnosis : Digraph models, diagnosability analysis and algorithms, distributed diagnosis. {6 classes}
7. Fault-tolerant VLSI based architectures : Interconnection networks, binary cube, graph networks, dynamic reconfiguration. {6 classes}